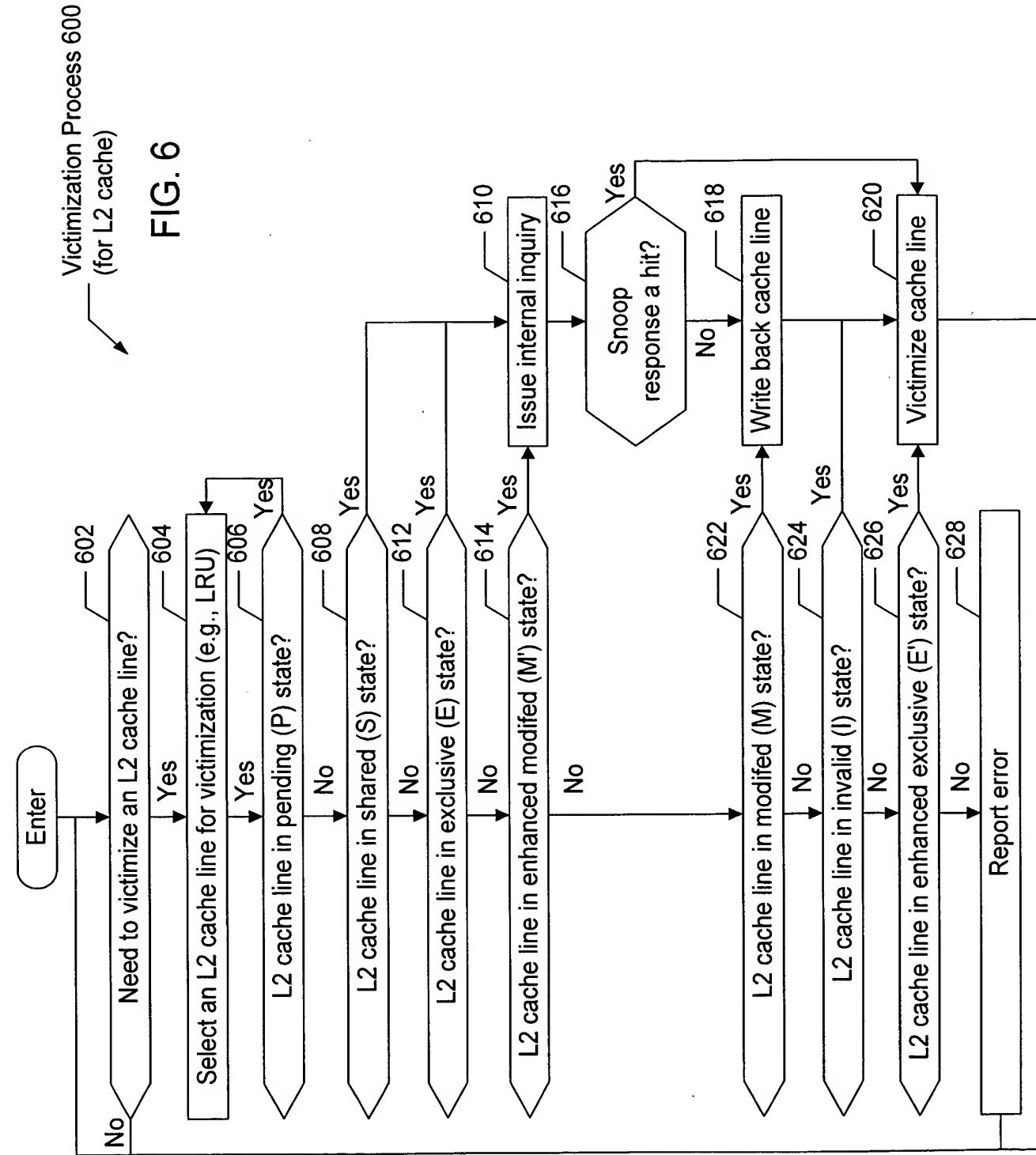




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Victimization Process 650
 (for L2 cache inclusive of
 L1 cache)

FIG. 6b

